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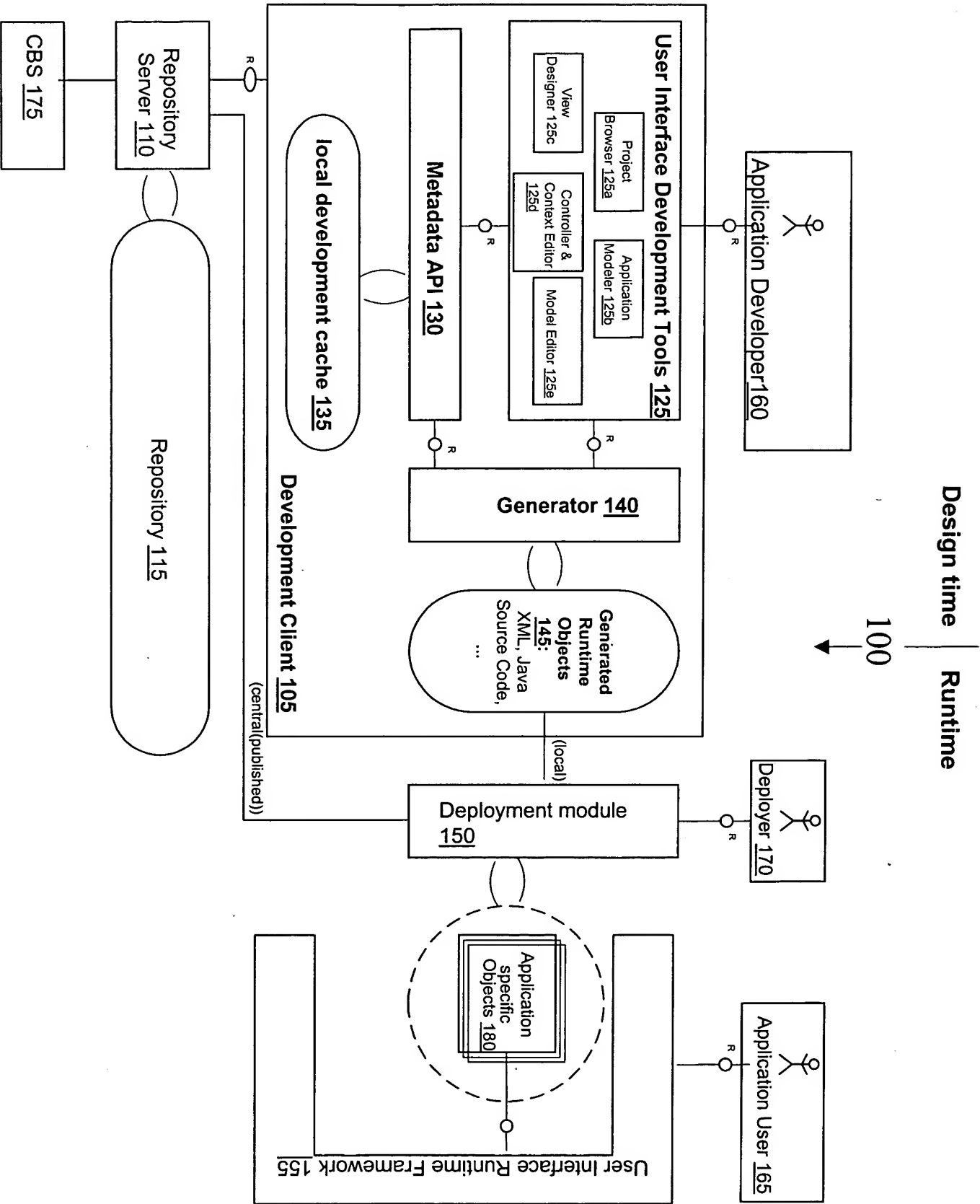
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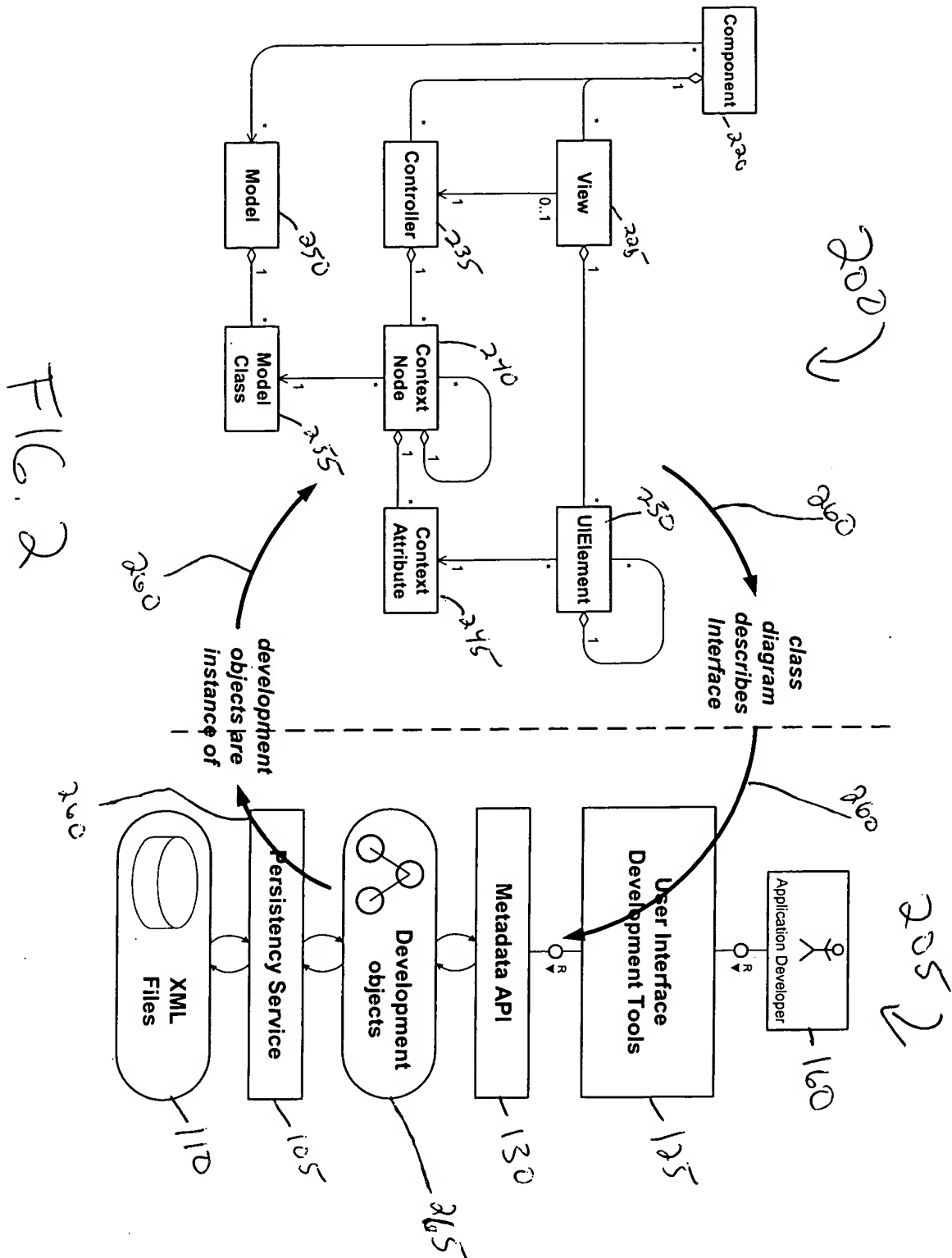
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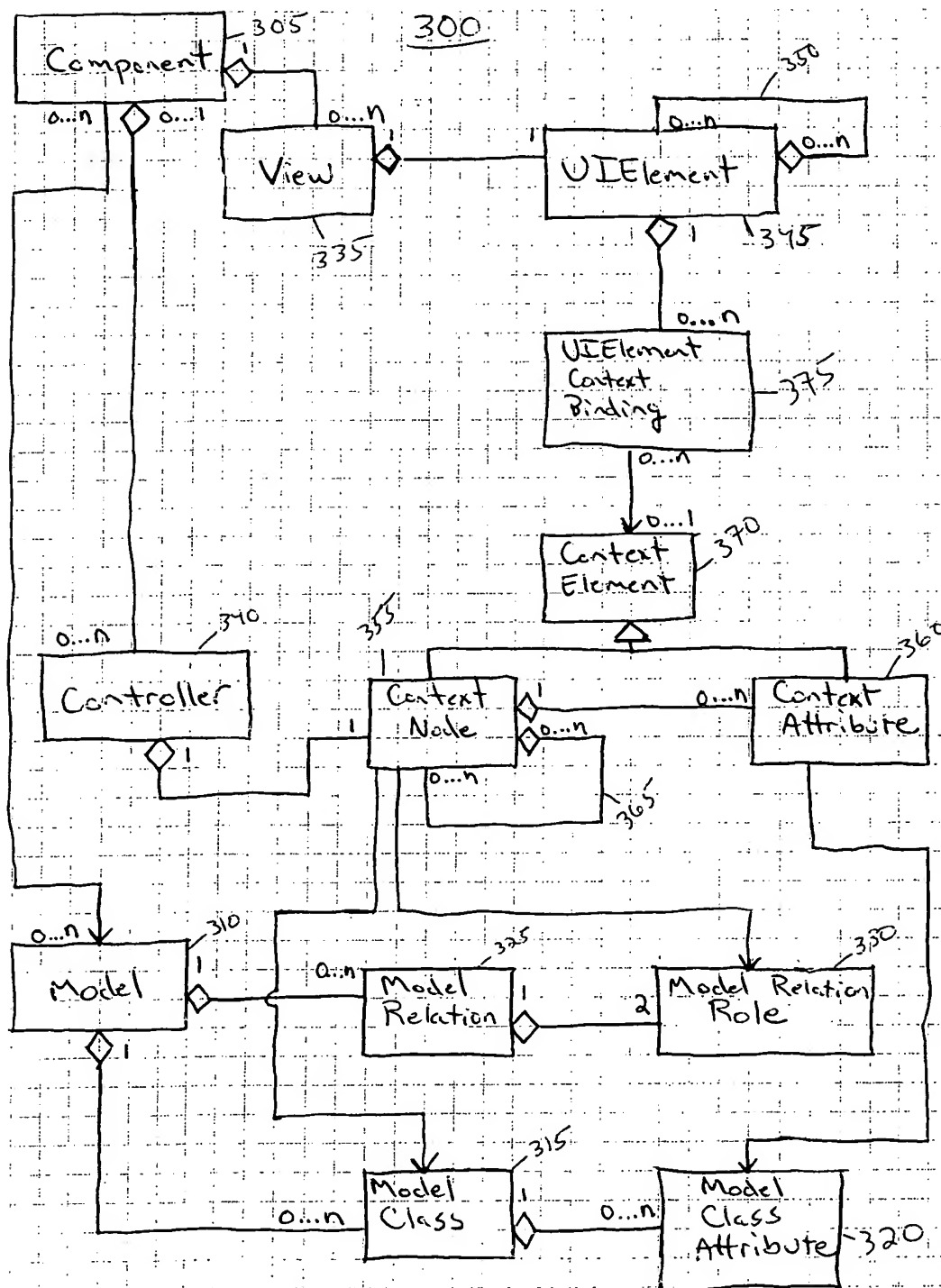
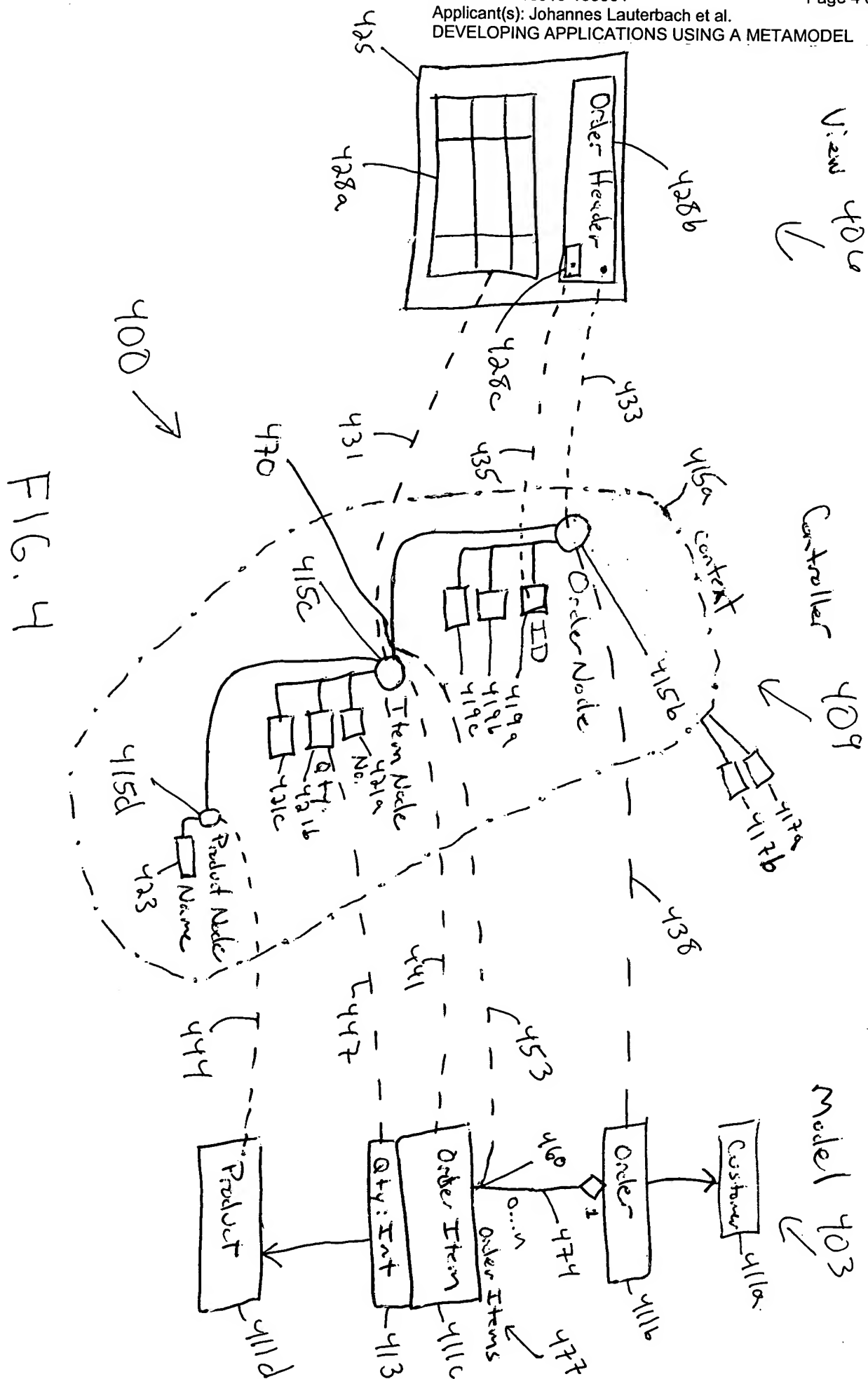
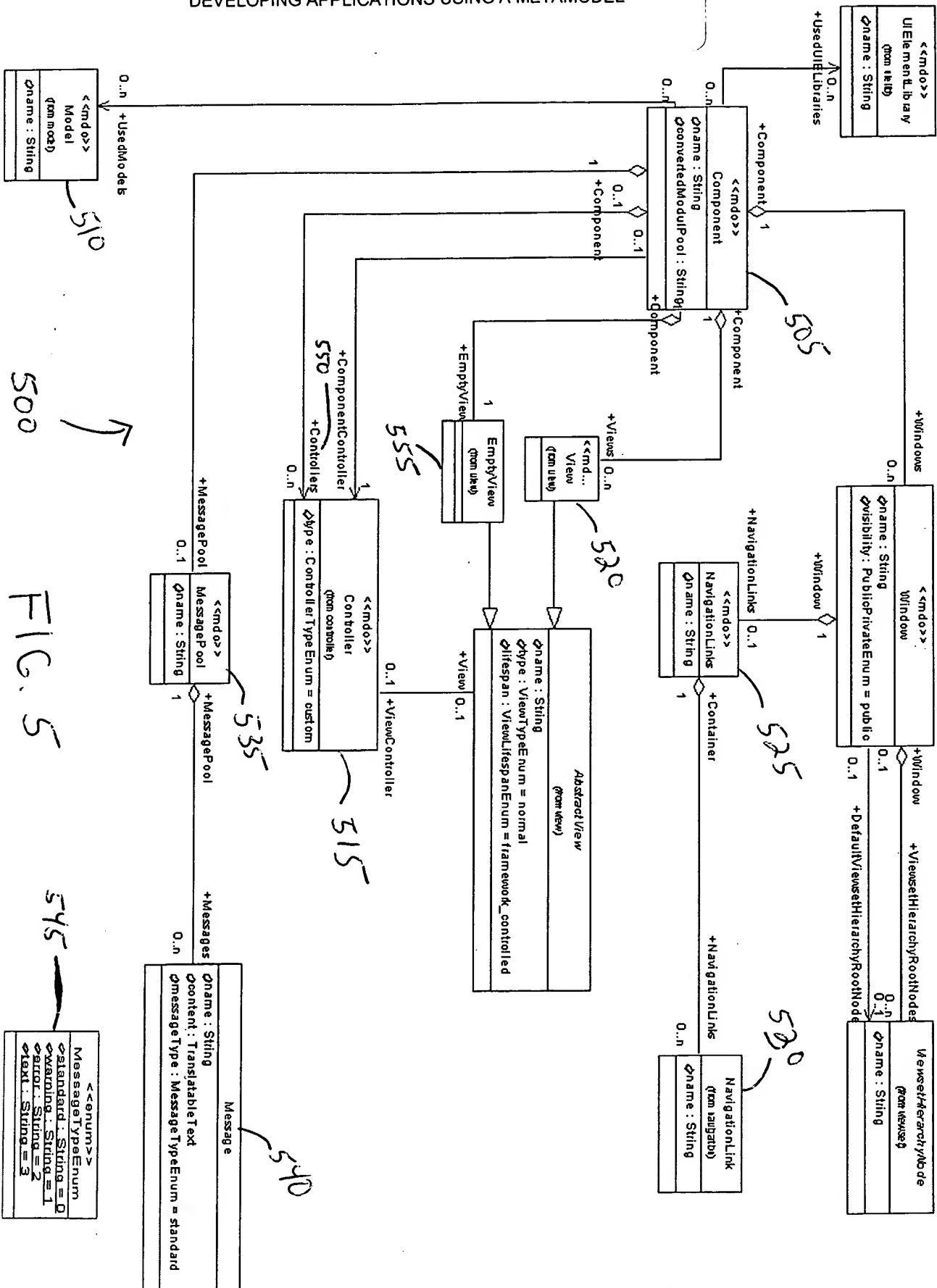


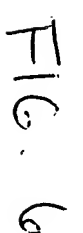
FIG. 3



Class Diagram: component / Components

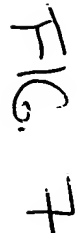


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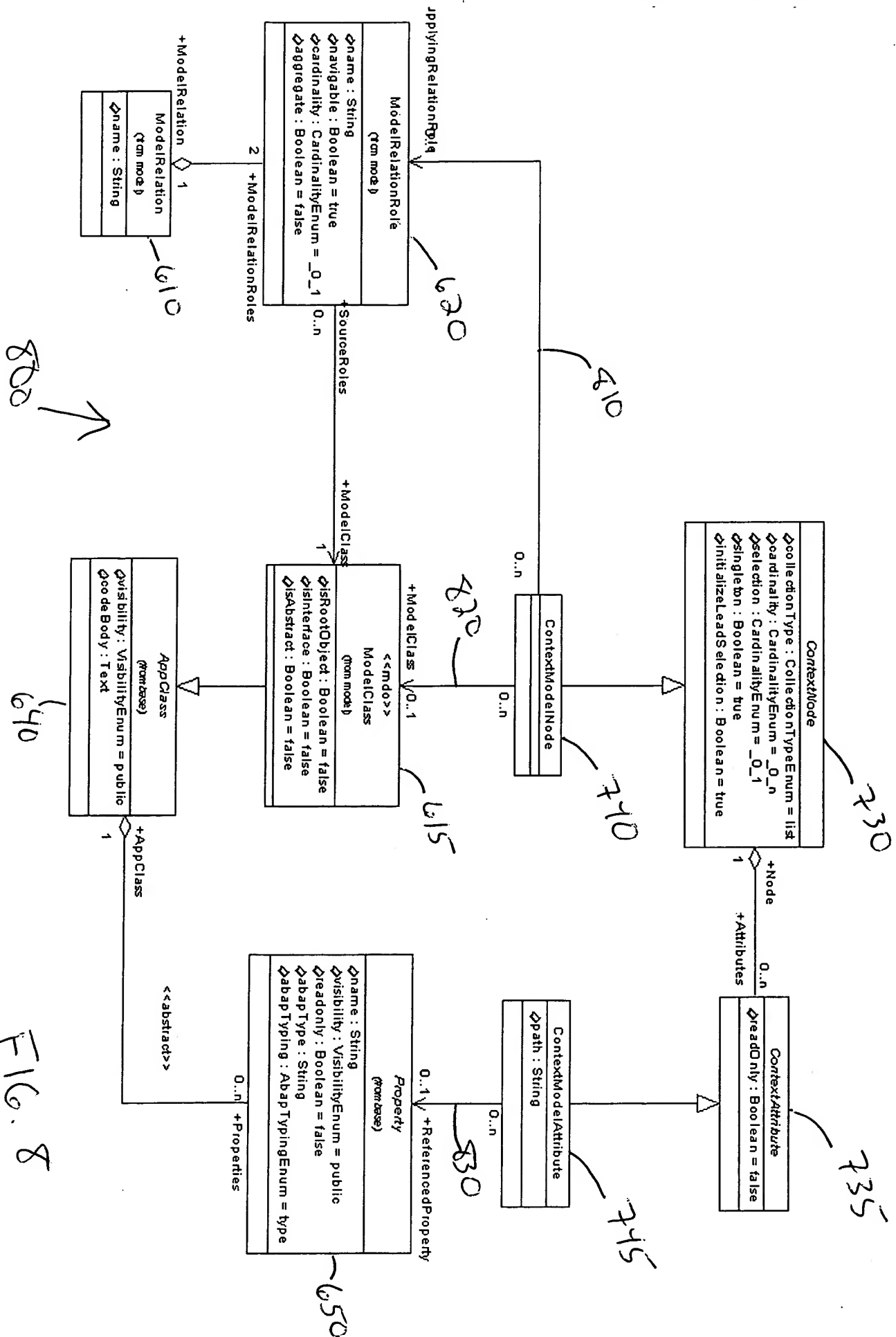


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Class Diagram: controller / Context (Model Nodes)



[illegible]

File. 9

Class Diagram: controller / Context Mapping

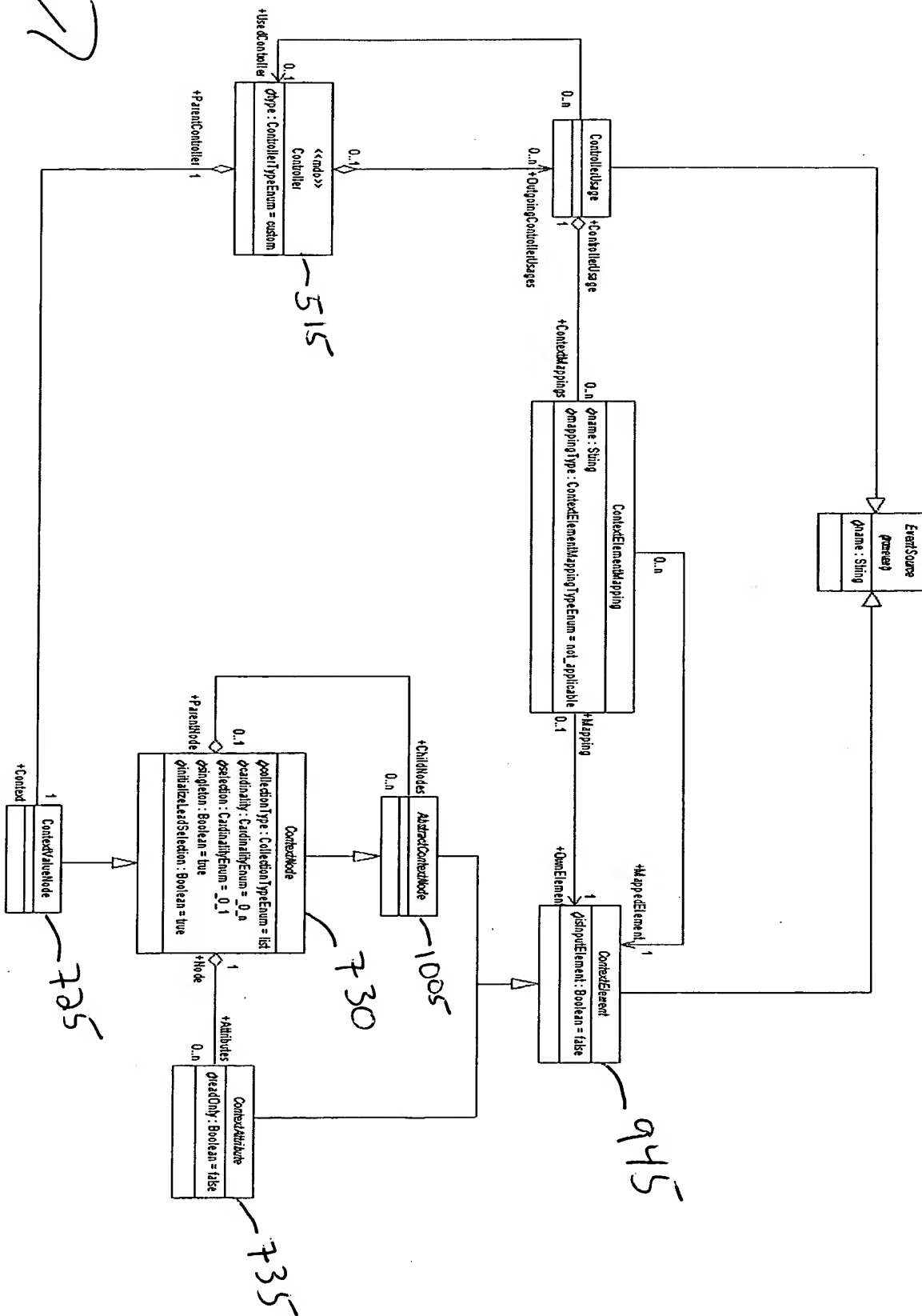


FIG. 10

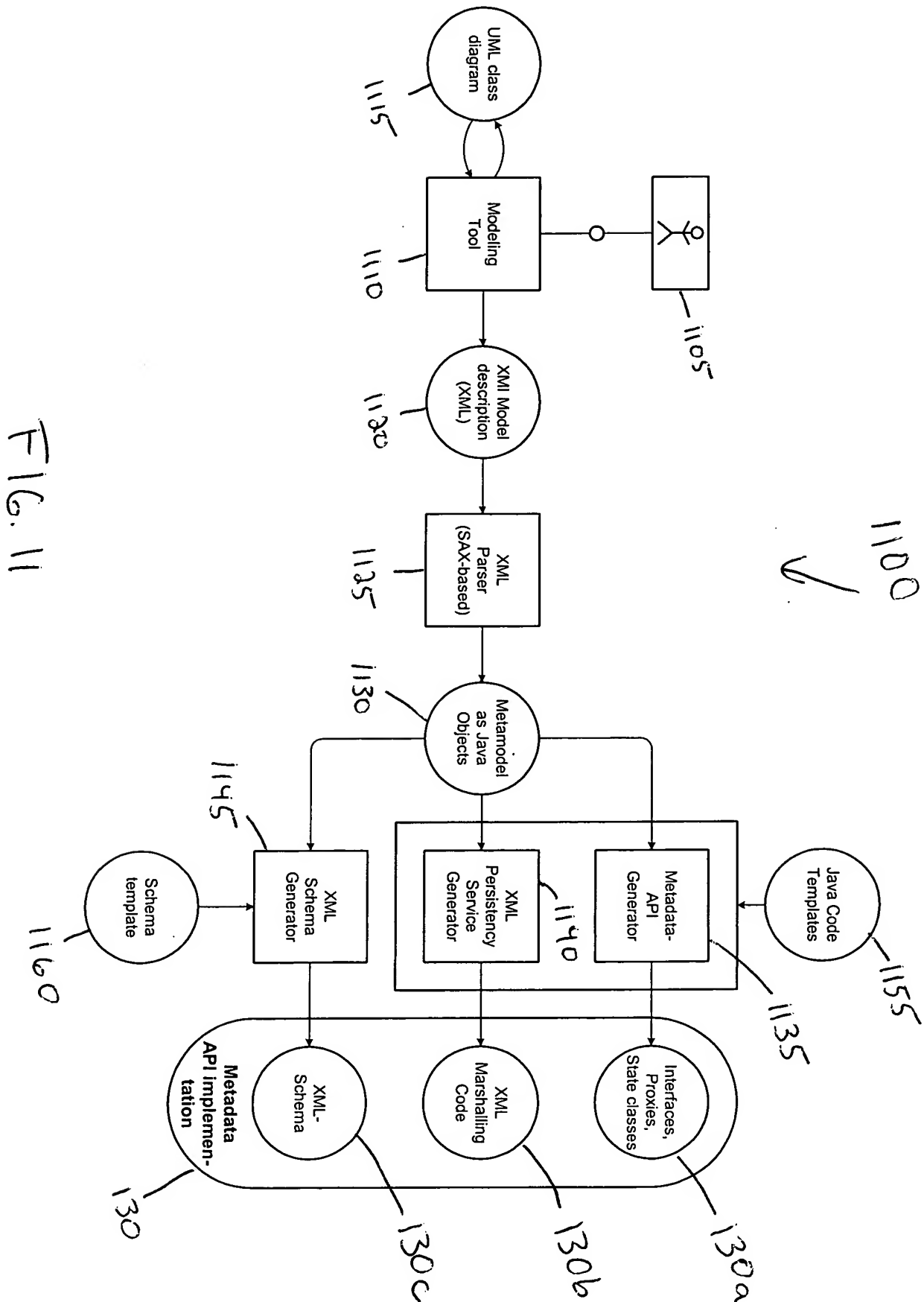
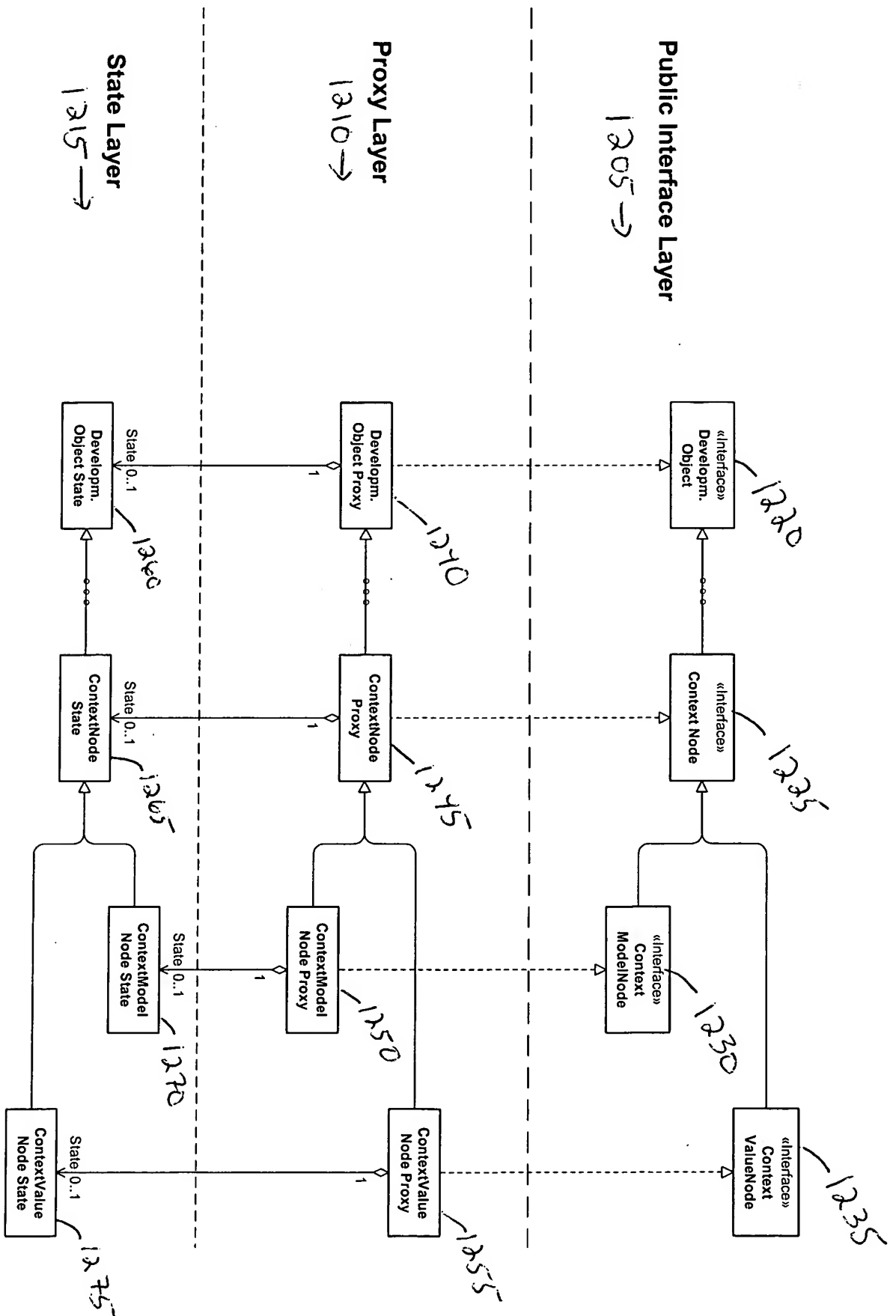


FIG. 11



File 12

1700

pure transient states

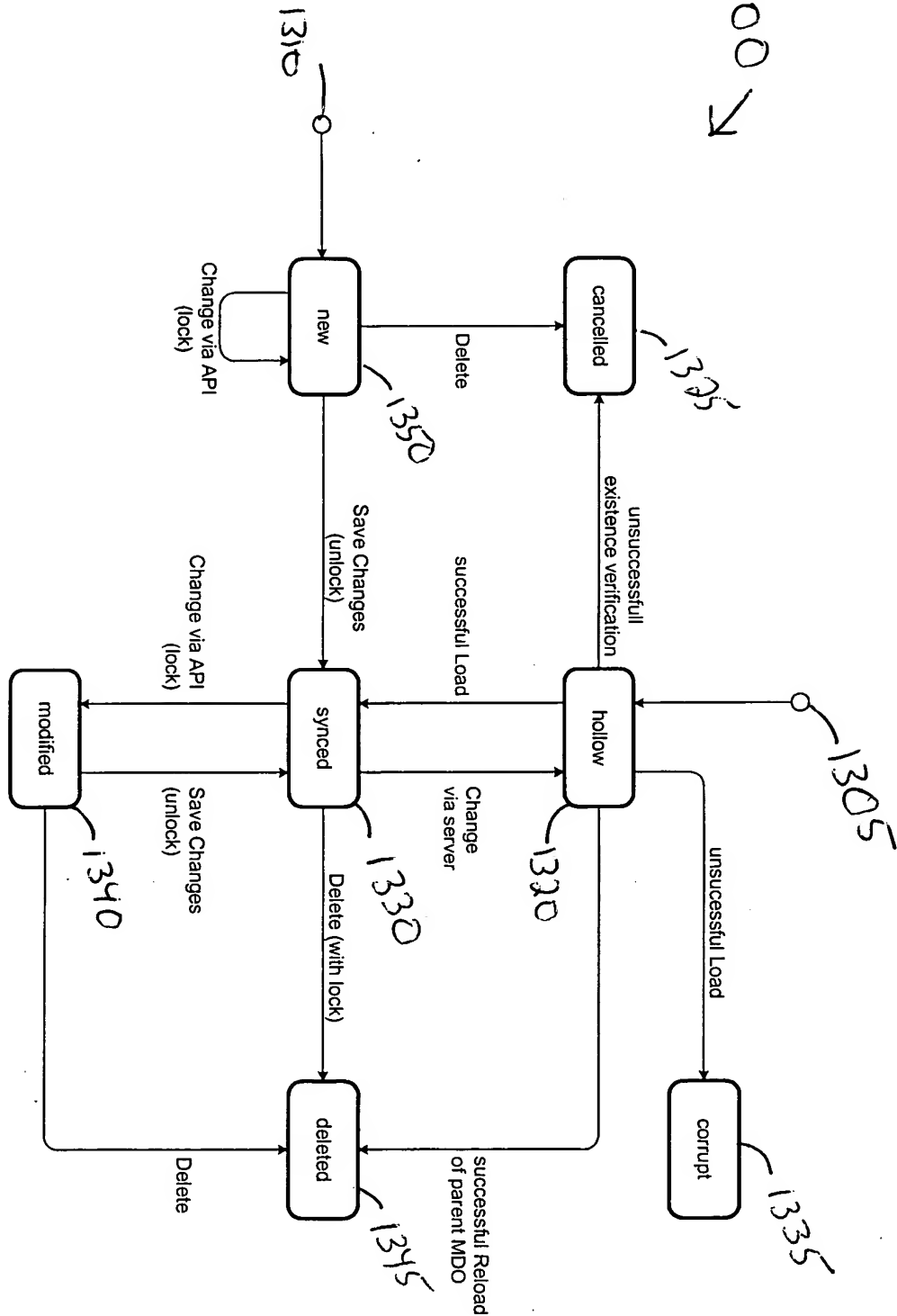


FIG. 13